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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/796,027	03/10/2004	Dong-Jin Park	1568.1092	2164
49455	7590	12/06/2005	EXAMINER	
STEIN, MCEWEN & BUI, LLP 1400 EYE STREET, NW SUITE 300 WASHINGTON, DC 20005			CHOWDHURY, TARIFUR RASHID	
			ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 12/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No.	Applicant(s)	
	10/796,027	PARK, DONG-JIN	
	Examiner	Art Unit	
	Tarifur R. Chowdhury	2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 08/03/05; 03/26/04.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

3. **Claims 8 and 12-15 are rejected under 35 U.S.C. 102(a) as being anticipated by applicant's admitted prior art (AAPA).**

4. The AAPA discloses and shows in Fig. 3, a field-sequential liquid crystal display panel, comprising:

- thin film transistors (332);
- cell electrodes (E11R -----E31B) respectively coupled to the drains of the thin film transistors;
- scan electrode lines (LS1-----LSn) coupled to the gates of the thin film transistors;
- data electrode lines (LD1-----LD3) coupled to the sources of the thin film transistors; and

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- storage capacitors (C11R --- C31B) provided between the cell electrodes and the scan electrode lines to sustain voltages applied to the cell electrodes;

As to claim 12, the AAPA described in the instant application also discloses (page 3, paragraph 0014) that the voltage is sustained in the storage capacitors between an ending point of scanning each of the respective scan electrode lines and a starting point of a lighting time which is applied to ones of the cell electrodes.

As to claims 13-14, the AAPA also discloses and shows in Fig. 5 that the display panel further comprising a glass substrate (51), wherein the scan electrode lines are provided on the glass substrate and an insulating layer provided on the data electrode lines and wherein the cell electrodes are formed on the insulating layer (page 4, paragraph 0015).

As to claim 15, it is also clear from Fig. 4 of the AAPA that the storage capacitors are formed by arranging the cell electrodes so that upper portions of the cell electrodes are disposed under the scan electrode lines.

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. **Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over applicant's admitted prior art (AAPA).**

7. As to claim 6, the AAPA described in the instant application discloses the capacitance to be 0.06 PF (which is very close to *approximately* 0.07 PF). Further, considering that the AAPA does not explicitly disclose that the capacitance of the storage capacitor is approximately 0.07 PF to 0.2 PF, it is common and known in the art to set the capacitance of the storage capacitor within the claimed range to optimize the performance of the display panel and thus would have been obvious.

8. Claims 1-7, 9 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over applicant's admitted prior art (AAPA) in view of Kyung Sik Jo, KR 1999-005264 (Document 1).

9. The AAPA discloses and shows in Fig. 3, a field-sequential liquid crystal display panel, comprising:

- thin film transistors (332), each comprising a drain (D), a source (S), and a gate (G);
- cell electrodes (E11R -----E31B) respectively coupled to the drains of the thin film transistors;
- scan electrode lines (LS1-----LSn) coupled to the gates of the thin film transistors;
- data electrode lines (LD1-----LD3) coupled to the sources of the thin film transistors; and
- storage capacitors (C11R --- C31B) to sustain voltages applied to the cell electrodes;

The AAPA differs from the claimed invention because it does not explicitly

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disclose that the are provided between each of the cell electrodes and a respective one of the scan electrode lines.

Document 1 discloses a previous-gate driving type liquid crystal display in which a TFT connected to an n-th gate line G_n is connected to pixel electrodes on an (n+2)th gate line $G_{(n+2)}$ to drive the pixel electrodes, and that a sustain capacitor (C_{st}) is formed between the pixel electrodes on the (n+2)th gate line $G_{(n+2)}$ and the (n+2)th gate line $G_{(n+2)}$. Document 1 further discloses that such an arrangement increases aperture ratio.

Document 1 is evidence that ordinary workers in the art would find a reason, suggestion or motivation to provide storage capacitors between each of the cell electrodes and a respective one of the scan electrode lines.

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention was made to modify the display panel of the AAPA by providing storage capacitors between each of the cell electrodes and a respective one of the scan electrode lines for advantages such as increased aperture ratio, as per the teachings of Document 1.

Accordingly, claims 1-3, 5, 9 and 10 would have been obvious.

As to claim 6, the AAPA described in the instant application discloses the capacitance to be 0.06 PF (which is very close to *approximately* 0.07 PF). Further, considering that the AAPA does not explicitly disclose that the capacitance of the storage capacitor is approximately 0.07 PF to 0.2 PF, it is common and known in the art

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to set the capacitance of the storage capacitor within the claimed range to optimize the performance of the display panel and thus would have been obvious.

As to claims 4 and 7, the AAPA also shows in Fig. 3 that display panel also comprising a data driver (55) and a scan driver (54) to drive the data electrode lines and the scan electrode lines respectively.

Conclusion

10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tarifur R. Chowdhury whose telephone number is (571) 272-2287. The examiner can normally be reached on M-Th (6:30-5:00) Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


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TRC

November 30, 2005



TARIFUR R. CHOWDHURY
PRIMARY EXAMINER